Ref #	Hits	Search Query	DBs	Default Operato r	Plural	Time Stamp
L1	955	361/82,84.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 10:05
L2	76	361/82,84.ccls. and (miswiring miswire reverse adj wiring reverse adj polarity)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 10:06
ь3	6	361/82,84.ccls. and (miswiring miswire reverse adj wiring reverse adj polarity) same trip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 10:14
L4	17	361/82,84.ccls. and (miswiring miswire reverse adj wiring reverse adj polarity) same time	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ОИ	2006/04/13 10:16
L5	1	361/82,84.ccls. and (miswiring miswire reverse adj wiring) same time	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 10:16
L6	43	"361"/\$.ccls. and (miswiring miswire reverse adj wiring) same time	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 12:18
L7	0	"361"/\$.ccls. and (miswiring miswire reverse adj wiring) same RC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	о́и	2006/04/13 10:26

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L8	16	6 and ground with "20"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13
L9	0	"361"/\$.ccls. and (miswiring miswire reverse adj wiring) same finlay.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13
L10	72	"361"/\$.ccls. and (miswiring miswire reverse adj wiring) same ground adj fault	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13
L11	5	"361"/\$.ccls. and (miswiring miswire reverse adj wiring) and finlay.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	OR	ON	2006/04/13 12:33
L12	14	"361"/\$.ccls. and (miswiring miswire reverse adj wiring) and gershen.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	OR	ON	2006/04/13
L13	713	circuit same interrupter same current near2 limiting	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 14:07
L14	19	circuit same interrupter same current near2 limiting same diode with resistor with capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 14:10
L15	3	13 and miswir\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 14:09

L16	0	13 and miswir\$3 same trip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 14:09
L17	213	circuit same interrupter same diode with resistor with capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 14:11
L18	0	17 same (miswir\$3 reverse near wir\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 14:11
L19	358	circuit same interrupter same diode with capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13
L20	0	19 same (miswir\$3 reverse near wir\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	OR	ON	2006/04/13 14:11
L21	4	19 and (miswir\$3 reverse near wir\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	OR	ON	2006/04/13 14:12
s1	2	"20040264078"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:05
S2	30893	circuit near3 (interruptor interrupter interrupting)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:07

\$3	1324	circuit near3 (interruptor interrupter interrupting) same detection	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:07
S4	211	circuit near3 (interruptor interrupter interrupting) same detection same line same load	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:07
S5	171	circuit near3 (interruptor interrupter interrupting) same detection same line same load and (miswiring reverse wire\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:08
\$6	61	circuit near3 (interruptor interrupter interrupting) same detection same line same load same (miswiring reverse wire\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:08
s7	23	circuit near3 (interruptor interrupter interrupting) same detection same line same load same (miswiring reverse near3 wire\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:09
S8	66	circuit near3 (interruptor interrupter interrupting) same detection same line same load and (miswiring reverse near3 wire\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:09
S9	29	circuit near3 (interruptor interrupter interrupting) same detection same line same load and (miswiring reverse near3 wire\$3)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:09
S10	10	circuit near3 (interruptor interrupter interrupting) same detection same line same load same (miswiring reverse near3 wire\$3)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:16

S11	21	(bernard and gershen).in. and interrupter	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:13
S12	0	(bernard and gershen).in. and "4595894".pn.	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:13
s13	2	"4595894".pn.	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:14
S14	23	circuit near3 (interruptor interrupter interrupting) same detection same line same load same (miswiring reverse near3 wire\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:17
s15	23	circuit near3 (interruptor interrupter interrupting) same detection same line same load same (miswiring reverse near3 wired)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:20
S16	7	circuit near3 ' (interruptor interrupter interrupting) same detection same line same load same (miswiring reverse near3 wired) and current same limit\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:23
S17	5	circuit near3 (interruptor interrupter interrupting) same detection same line same load same (miswiring reverse near3 wired) and current near3 limit\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:23
S18	5	circuit near3 (interruptor interrupter interrupting) same detection same line same load same (miswiring reverse near3 wired) and current near2 limit\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 10:05